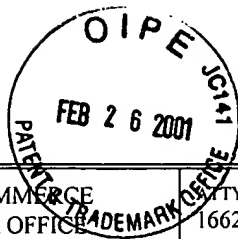


FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (Modified) PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (Use several sheets if necessary)  (37 CFR 1.98(b))	ATTY. DOCKET NO. 1662-27300 (P00-3094)	SERIAL NO. 09/652/59
APPLICANTS Shubhendu S. MUKHERJEE et al.		RECEIVED MAR 01 2001 Technology Center 2100
FILING DATE August 31, 2000		GRO 2152

U. S. PATENT DOCUMENTS													
Examiner Initial		Patent Number								Issue Date	Patentee	Class	Filing Date (If Appropriate)
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QN	AE	5	7	8	7	4	8	0		07/28/98	Scales et al.	711	07/17/96
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FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION													
		Document Number								Publication Date	Country or Patent Office	Class	Translation
													Yes No



FORM PTO-1449 (Modified)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	APP. DOCKET NO. 1662-27300 (P00-3094)	SERIAL NO. 09/652,322
INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (Use several sheets if necessary)		APPLICANTS Shubhendu S. MUKHERJEE et al.	
(37 CFR 1.98(b))		FILING DATE August 31, 2000	GROUP 2152

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QW	AT	<i>Alpha Architecture Reference Manual</i> , Third Edition, The Alpha Architecture Committee, 1998 Digital Equipment Corporation (21 p.), in particular pages 3-1 through 3-15
QW	AU	<i>A Logic Design Structure For LSI Testability</i> , E. B. Eichelberger et al., 1977 IEEE (Pages 462-468)
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QW	BC	<i>End-To-End Fault Containment In Scalable Shared-Memory Multiprocessors</i> , D. Teodosiu, July 2000 (148 p.)
Examiner <i>[Signature]</i> Date Considered <i>1/7/2004</i>		
EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		